

ET 1 50396423 VJ

Figure 1

Dreps et al.
AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
Page 1 of 5

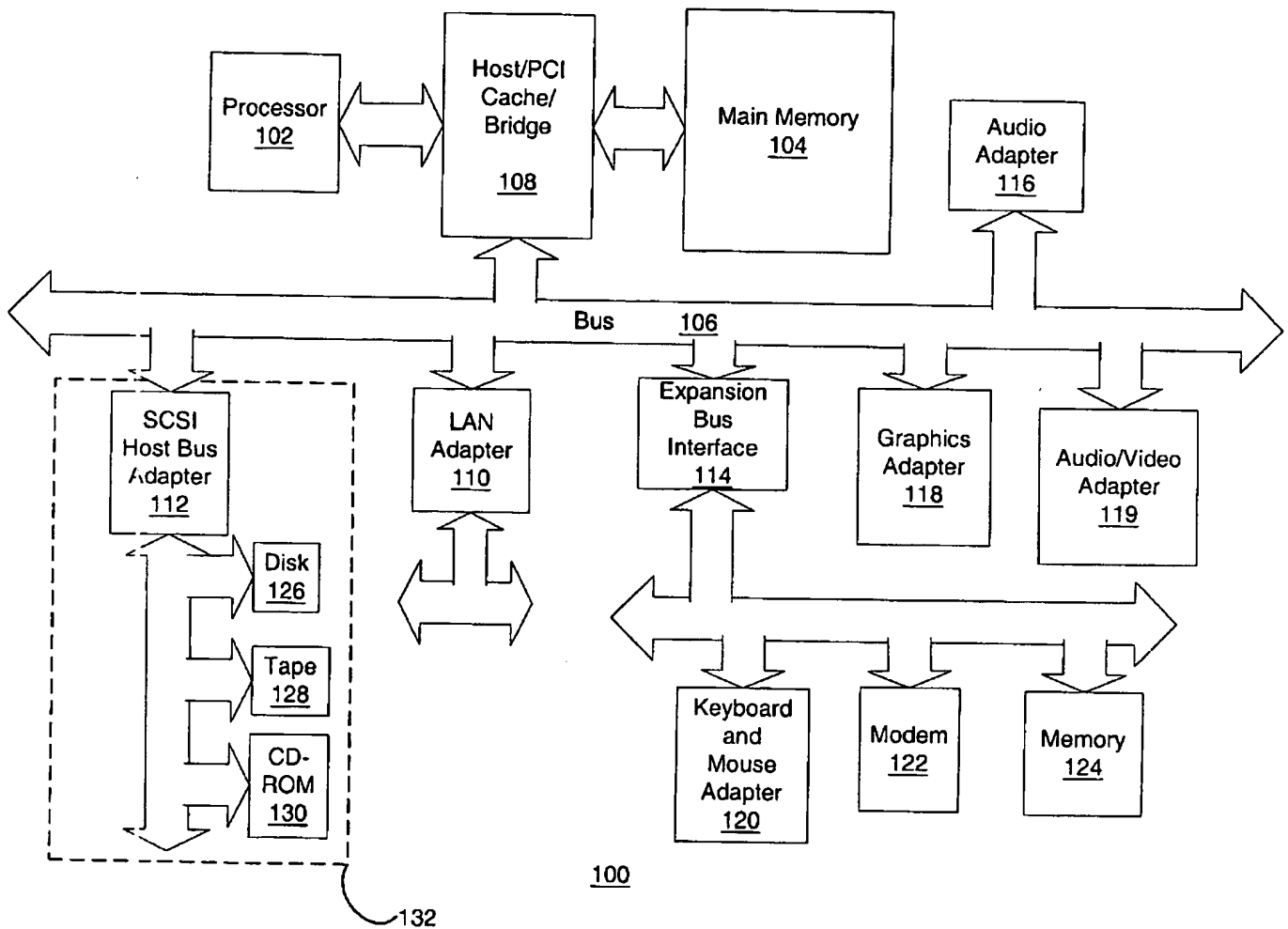


Figure 2

Dreps et al.
AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
Page 2 of 5

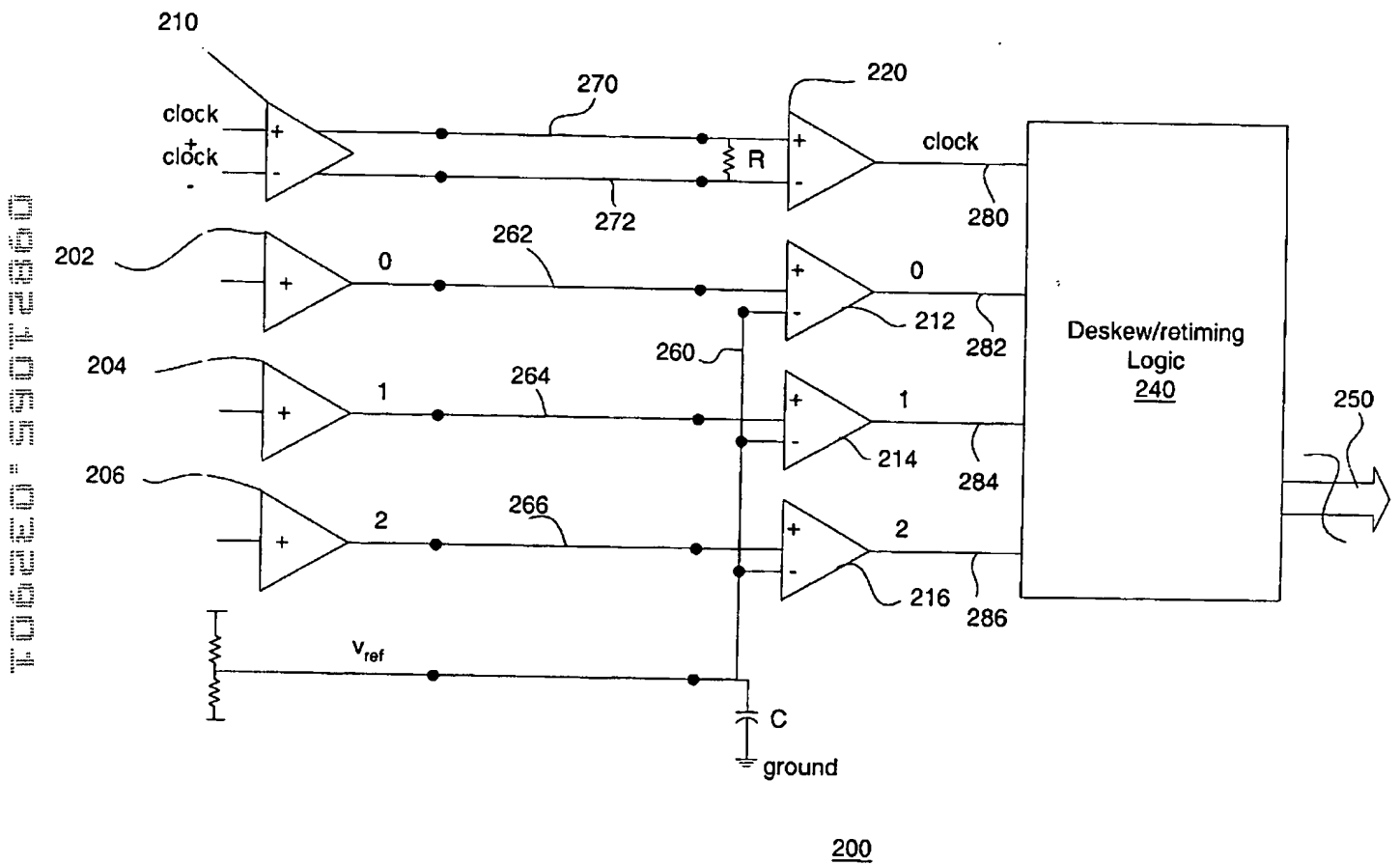


Figure 3

Dreps et al.
AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
Page 3 of 5

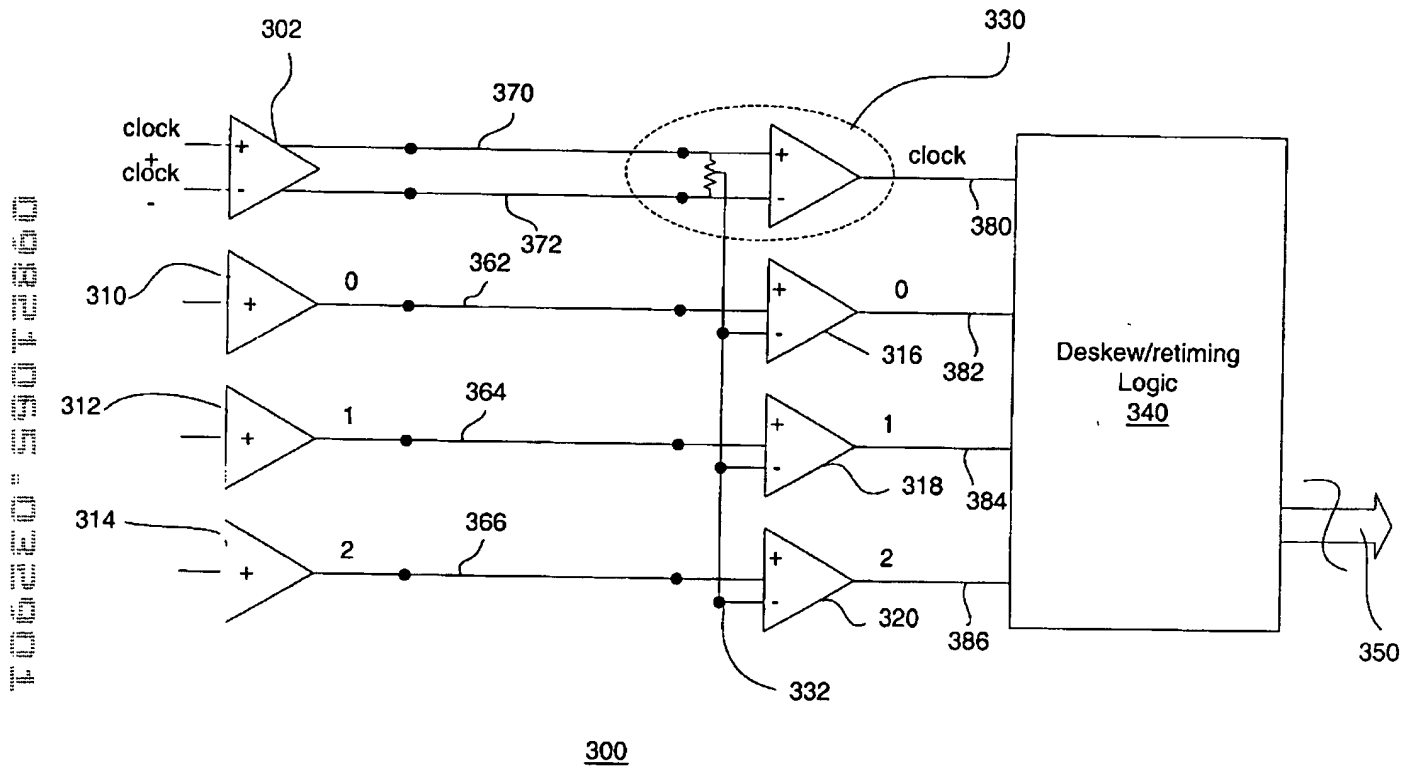


Figure 4

Dreps et al.
AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
Page 4 of 5

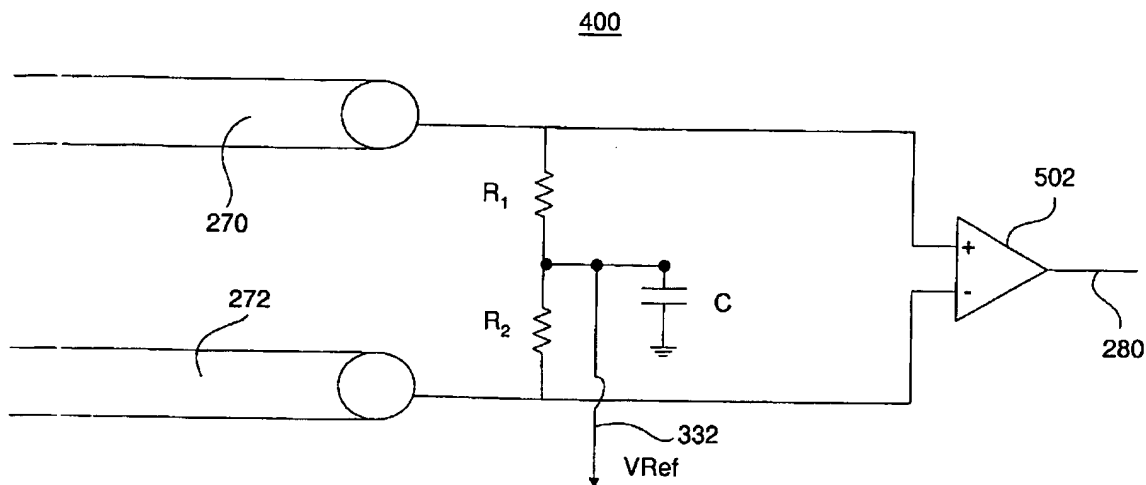


Figure 5

Dreps et al.
AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
Page 4 of 5

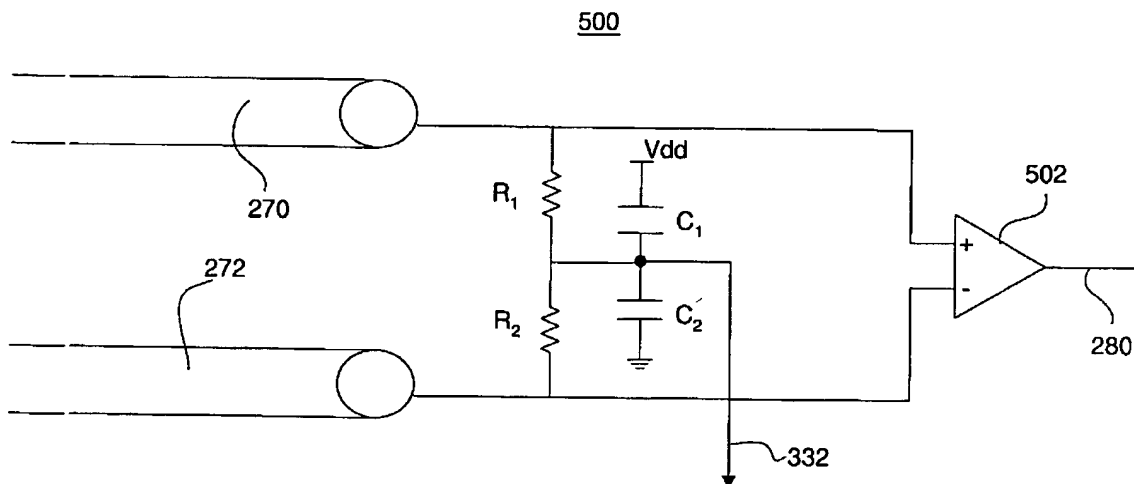


Figure 6

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AUS920000726US1
Pseudo-Differential Parallel
Source Synchronous Bus
Page 5 of 5

